

 <p>U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE</p> <p>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>USE SEVERAL SHEETS IF NECESSARY)</p>	<p>ATTY. DOCKET NO. NETL.001C1</p>	<p>APPLICATION NO. 10/768,534</p>
<p>APPLICANTS Jayesh R. Bhakta et al.</p>		
<p>FILING DATE January 30, 2004</p>		
<p>GROUP Unknown</p>		

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**EXAMINER
INITIAL**

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

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EXAMINER

DATE CONSIDERED

***EXAMINER, INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT**

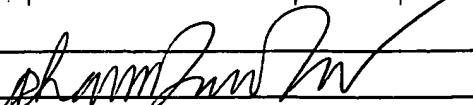
FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. NETL.001C1	APPLICATION NO. 10/768,534
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U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
LP	29	5,953,738	09/14/99	Rao		
	30	5,973,951	10/26/99	Bechtolsheim et al.		
	31	6,021,048	02/01/00	Smith		
	32	6,072,744	06/06/00	Kwean		
	33	6,097,619	08/01/00	Nguyen et al.		
	34	6,151,235	11/21/00	Kitagawa et al.		
	35	6,157,538	12/05/00	Ali et al.		
	36	6,181,004 B1	01/30/01	Koontz et al.		
	37	6,215,718 B1	04/10/01	Koelling		
	38	6,222,737 B1	04/24/01	Ross		
	39	6,222,739 B1	04/24/01	Bhakta et al.		
	40	6,353,539 B1	03/05/02	Horine et al.		
	41	6,502,161 B1	12/31/02	Perego et al.		
	42	6,532,158 B1	03/11/03	Buttet		
	43	6,545,895 B1	04/08/03	Li et al.		
	44	2002/0088633 A1	07/11/02	Kong et al.		
	45	2003/0014578 A1	01/16/03	Pax		
	46	2003/0048616 A1	03/13/03	Ko et al.		
	47	2003/0051091 A1	03/13/03	Leung et al.		
	48	2003/0057564 A1	03/27/03	Leedy		
	49	2003/0061447 A1	03/27/03	Perego et al.		
	50	2003/0090879 A1	05/15/03	Doblar et al.		

EXAMINER		DATE CONSIDERED	01/06/2004
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U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
1	4,280,176	07/21/81	Tan			
2	4,679,167	07/07/87	Finnell			
3	5,012,389	04/30/91	Perry			
4	5,164,916	11/17/92	Wu et al.			
5	5,191,404	03/02/93	Wu et al.			
6	5,226,136	07/06/93	Nakagawa			
7	5,303,123	04/12/94	Chandler et al.			
8	5,383,148	01/17/95	Testa et al.			
9	5,412,538	05/02/95	Kikinis et al.			
10	5,428,762	06/27/95	Curran et al.			
11	5,465,229	11/07/95	Bechtolsheim et al.			
12	5,495,435	02/27/96	Sugahara			
13	5,513,135	04/30/96	Dell et al.			
14	5,532,954	07/02/96	Bechtolsheim et al.			
15	5,541,448	07/30/96	Carpenter			
16	5,572,691	11/05/96	Koudmani			
17	5,631,807	05/20/97	Griffin			
18	5,642,323	06/24/97	Kotani et al.			
19	5,652,462	07/29/97	Matsunaga et al.			
20	5,652,861	07/29/97	Mayo et al.			
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24	5,737,192	04/07/98	Linderman			
25	5,754,408	05/19/98	Derouiche			
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27	5,847,985	12/08/98	Mitani et al.			
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 08/02/2004	
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
LP	51	JP 60034054 A	02/21/85	Japan				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
LP	52	PCT International Search Report dated August 29, 2003 for counterpart PCT application No. PCT US/03/06978 filed March 6, 2003, in 5 pages.
	53	JEDEC Standard No. 21-C, 4.20.2 - 168 Pin, PC133 SDRAM Registered DIMM Design Specification, Revision 1.4, Release 11a, February 2002.
	54	JEDEC Standard No. 21-C, 4.20.3 - 144 Pin, PC133 SDRAM Unbuffered SO-DIMM, Reference Design Specification, Revision 1.02, Release 11.
	55	JEDEC Standard No. 21-C, DDR SDRAM PC2100 and PC1600 DDR SDRAM Registered DIMM Design Specification, Revision 1.3, Release 11b, January 2002.
	56	JEDEC Standard No. 21-C, 4.20.5 - 184 Pin PC1600/2100 DDR SDRAM Unbuffered DIMM Design Specification, Revision 1.1, Release 11b.
	57	JEDEC Standard No. 21-C, 4.20.6 - 200 Pin, PC2700/PC2100/PC1600 DDR SDRAM Unbuffered SO-DIMM Reference Design Specification, Revision 1.1, Release 11b, April 26, 2002.
	58	Intel, PC SDRAM Registered DIMM Design Support Document, Revision 1.2, October 1998.
↓	59	Intel Corporation, 66/100 MHz PC SDRAM 64-Bit Non-ECC/Parity 144 Pin Unbuffered SO-DIMM Specification, Revision 1.0, February 1999.

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